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IEEE STD IEEE Standard

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- ☐ 2. **Data monitoring in high-performance clusters for computing applications**
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- ☐ 4. **A VLSI chip set for burst and fast ATM switching**
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- ☐ 5. **The ATLAS level-1 central trigger processor core module (CTP/spl i.bar/C**
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Digital Object Identifier 10.1109/LCOMM.2005.1506720
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- ☐ **7. Pulsed para-neural network (PPNN) synthesis in a 3-D cellular automata**
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